

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

priority Application Serial No. 09/036,701
priority Filing Date March 6, 1998
Inventor Werner Juengling
Assignee Micron Technology, Inc.
priority Group Art Unit 2812
priority Examiner H. Tsai
Attorney's Docket No. MI22-1243
Title: Semiconductor Processing Methods of Forming Devices on a Substrate,
Forming Device Arrays on a Substrate, Forming Conductive Lines on a
Substrate, and Forming Capacitor Arrays on a Substrate, and Integrated
Circuitry

PRELIMINARY AMENDMENT

To: BOX PATENT APPLICATION
Assistant Commissioner for Patents
Washington, D.C. 20231

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AMENDMENTS

In the Specification

At p. 1 before the "Technical Field" section, please insert the
following:

--RELATED PATENT DATA

This patent resulted from a divisional application of U.S. Patent
Application Serial No. 09/036,701, filed March 6, 1998, which is a
US Pat. No. 6,482,731